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| **National University of Computer and Emerging Sciences, Lahore Campus** | | | | |
| C:\Users\saif\AppData\Local\Microsoft\Windows\Temporary Internet Files\Content.Word\final design.jpg | **Course:** | **Digital Logic Design** | **Course Code:** | **EE1005** |
| **Program:** | **BS (Computer Science)** | **Semester:** | **Spring 2022** |
| **Duration:** | **10 Minutes** | **Total Marks:** | **10** |
| **Paper Date:** | **09-03-2022** | **Weight** |  |
| **Section:** | **A** | **Page(s):** | **1** |
| **Exam:** | **Quiz 1** | **Reg. No.** |  |
| **Instruction/Notes:** | **Plagiarism will be dealt seriously causing an F in course** | | | |

Q1. (a)What is the largest integer number that can be saved in a 16-bit register? Show your work.

(b)What is the smallest integer number that can be saved in a 16-bit register? Show your work.

Note: Assume that the numbers in part (a) and (b) are stored in 2’s complement form. (5)

Q2. Hexadecimal multiplication F0A×C3= (5)